

**RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE - EXAMINING GROUP 2827**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: HANZAWA, SATORU Art Unit: 2827

Appln. No.: 10/579,911 Examiner: Hidalgo,
Fernando N.

Filed: April 9, 2007 Conf. No.: 6903

For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

AMENDMENT AFTER FINAL REJECTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of January 6, 2009,
please amend the above-identified patent application as
indicated below.

Amendments to the claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.